

Translation

PATENT COOPERATION TREATY

PCT/JP2003/008434



PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 380300049WO1	FOR FURTHER ACTION See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416)	
International application No. PCT/JP2003/008434	International filing date (day/month/year) 03 July 2003 (03.07.2003)	Priority date (day/month/year)
International Patent Classification (IPC) or national classification and IPC G06K 19/07, 19/077, G06F 3/06, 3/08		
Applicant RENESAS TECHNOLOGY CORP.		

<p>1. This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36.</p> <p>2. This REPORT consists of a total of <u>6</u> sheets, including this cover sheet.</p> <p><input checked="" type="checkbox"/> This report is also accompanied by ANNEXES, i.e., sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT).</p> <p>These annexes consist of a total of <u>7</u> sheets.</p>	
<p>3. This report contains indications relating to the following items:</p> <p>I <input checked="" type="checkbox"/> Basis of the report</p> <p>II <input type="checkbox"/> Priority</p> <p>III <input type="checkbox"/> Non-establishment of opinion with regard to novelty, inventive step and industrial applicability</p> <p>IV <input checked="" type="checkbox"/> Lack of unity of invention</p> <p>V <input checked="" type="checkbox"/> Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement</p> <p>VI <input type="checkbox"/> Certain documents cited</p> <p>VII <input type="checkbox"/> Certain defects in the international application</p> <p>VIII <input type="checkbox"/> Certain observations on the international application</p>	

Date of submission of the demand 03 July 2003 (03.07.2003)	Date of completion of this report 06 April 2004 (06.04.2004)
Name and mailing address of the IPEA/JP	Authorized officer
Facsimile No.	Telephone No.

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

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I. Basis of the report

1. With regard to the elements of the international application:*

- ☐ the international application as originally filed
- ☒ the description:
pages _____ 1-53 _____, as originally filed
pages _____, filed with the demand
pages _____, filed with the letter of _____
- ☒ the claims:
pages _____ 2-35,38,40-44,46,49-75 _____, as originally filed
pages _____, as amended (together with any statement under Article 19
pages _____, filed with the demand
pages _____ 1,36,37,39 _____, filed with the letter of _____ 26 December 2003 (26.12.2003)
- ☒ the drawings:
pages _____ 1/44-44/44 _____, as originally filed
pages _____, filed with the demand
pages _____, filed with the letter of _____
- ☐ the sequence listing part of the description:
pages _____, as originally filed
pages _____, filed with the demand
pages _____, filed with the letter of _____

2. With regard to the language, all the elements marked above were available or furnished to this Authority in the language in which the international application was filed, unless otherwise indicated under this item.
These elements were available or furnished to this Authority in the following language _____ which is:

- ☐ the language of a translation furnished for the purposes of international search (under Rule 23.1(b)).
- ☐ the language of publication of the international application (under Rule 48.3(b)).
- ☐ the language of the translation furnished for the purposes of international preliminary examination (under Rule 55.2 and/or 55.3).

3. With regard to any nucleotide and/or amino acid sequence disclosed in the international application, the international preliminary examination was carried out on the basis of the sequence listing:

- ☐ contained in the international application in written form.
- ☐ filed together with the international application in computer readable form.
- ☐ furnished subsequently to this Authority in written form.
- ☐ furnished subsequently to this Authority in computer readable form.
- ☐ The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.
- ☐ The statement that the information recorded in computer readable form is identical to the written sequence listing has been furnished.

4. ☒ The amendments have resulted in the cancellation of:

- ☐ the description, pages _____
- ☒ the claims, Nos. _____ 45,47,48 _____
- ☐ the drawings, sheets/fig _____

5. ☐ This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**

* Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report since they do not contain amendments (Rule 70.16 and 70.17).

** Any replacement sheet containing such amendments must be referred to under item 1 and annexed to this report.

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IV. Lack of unity of invention

1. In response to the invitation to restrict or pay additional fees the applicant has:

- ☐ restricted the claims.
- ☐ paid additional fees.
- ☐ paid additional fees under protest.
- ☐ neither restricted nor paid additional fees.

2. ☒ This Authority found that the requirement of unity of invention is not complied with and chose, according to Rule 68.1, not to invite the applicant to restrict or pay additional fees.

3. This Authority considers that the requirement of unity of invention in accordance with Rules 13.1, 13.2 and 13.3 is

- ☐ complied with.
- ☒ not complied with for the following reasons:

The subject matter of claims 1-44 and 46 concerns a multi-function card device having an interface controller and security controller.

The subject matter of claims 49-62 concerns a semiconductor card device having an antenna.

The subject matter of claims 63-71 concerns an external terminal of a semiconductor card device.

The subject matter of claims 72-75 concerns a semiconductor integrated circuit.

4. Consequently, the following parts of the international application were the subject of international preliminary examination in establishing this report:

- ☒ all parts.
- ☐ the parts relating to claims Nos. _____

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V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Statement

Novelty (N)	Claims	5-9, 11, 14-35, 40-44, 46, 49-75	YES
	Claims	1-4, 10, 12, 13, 36-39	NO
Inventive step (IS)	Claims	6-9, 11, 41-44, 46, 49-75	YES
	Claims	1-5, 10, 12-40	NO
Industrial applicability (IA)	Claims	1-44, 46, 49-75	YES
	Claims		NO

2. Citations and explanations

Document 1: WO, 2001/84490, A1 (Hitachi, Ltd., Hitachi ULSI Systems Co., Ltd.), 08 November, 2001

Document 2: JP, 2002-351623, A (Fujitsu, Ltd.), 06 December, 2002

Claims 1-4

Document 1 describes the multi-function device configuration described in claims 1-4.

Claim 5

The configuration of an IC card having an internal antenna is described in paragraphs 0027 to 0030 and Fig. 3 in document 2, and employing this in the multi-function card device described in document 1 would be easy for a person skilled in the art.

Claims 6-9

Selectively connecting an internal antenna and an external antenna to a security controller is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.

Claim 10

Document 1 describes the multi-function card device configuration described in claim 10.

Claim 11

Shielding a power supply of a security controller is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.

Claims 12 and 13

Document 1 describes the multi-function card device configuration described in claims 12 and 13.

Claim 14

A divided ground pattern is a well known art, and applying it to the multi-function card device described in documents 1 and 2 would be easy for a person skilled in the art.

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Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of Box V. 2:

Claim 15

Stacking semiconductor chips on ferrite plates is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 16

Making a ferrite plate with a ferrite chip, an applied ferrite paste, and a pasted ferrite film is a well known art, and adopting it in the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 17

Disposing a ferrite plate on a central section of an antenna is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 18

Disposing a semiconductor chip on the side of an antenna and covering it with a metallic cap is a well-known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 19

Forming an antenna with a coil pattern or a coil is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 20

Making an antenna with a dielectric chip and stacking it on a ferrite plate is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 21

Stacking semiconductor chips on a ferrite plate, on a side opposite a stacking side of a dielectric antenna chip is a design item that could be selected as appropriate by a person skilled in the art.

Claim 22

Covering an external connection terminal with a cap and making a cap with a cap mixed with ferrite or a metallic cap is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 23

Covering an external connection terminal with casing, stacking semiconductor chips on a ferrite plate, and providing with a magnetic shield on an opposite side of a receiving side of an antenna is a well-known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 24

Making a magnetic shield into the configuration of claim 24 is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

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Supplemental Box

(To be used when the space in any of the preceding boxes is not sufficient)

Continuation of Box V. 2:

Claim 25

Making a casing into the configuration of claim 25 is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 26

Attaching a condenser for tuning to an antenna is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 27

Making a condenser for tuning into the configuration of claim 24 is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claims 28-35

Making a terminal section of a multi-function card device into the configuration of claims 28-35 is a well known art, and applying it to the multi-function card device described in document 1 and 2 would be easy for a person skilled in the art.

Claim 36-39

Document 1 describes the multi-function card device configuration described in claims 36-39.

Claim 40

Paragraphs 0027-0030 of document 3 and Fig. 3 describe a configuration of an IC card having an internal antenna, and applying it to the multi-function card device described in document 1 would be easy for a person skilled in the art.

Claim 41-44

Selectively connecting an internal antenna and external antenna to a security controller is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.

Claim 46

Shielding a security controller power supply is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.

Claim 49-71

The configuration of the semiconductor card device of claims 49-71 is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.

Claim 72-75

The configuration of the semiconductor card device of claims 72-75 is not described in any of the documents cited in the ISR, nor is it obvious to a person skilled in the art.